

MAY 17 2004

800.0052
A1141

OFFICIAL

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Marchand et al.

Serial No.: 09/853,989

Filed: May 11, 2001

For: METHODS AND APPARATUS FOR POWER CONTROL IN A SCALABLE
ARRAY OF PROCESSOR ELEMENTS

Group: 2116

Examiner: Perveen, Rehana

Durham, North Carolina

May 17, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATION OF FACSIMILE TRANSMISSION

Sirs:

I hereby certify that this correspondence is being facsimile transmitted to the United
States Patent and Trademark Office, Fax. No. 703-872-9306 on the date set forth below

1. Response to Restriction Requirement (2 pages)

Marianna Tortorelli

Printed name of person signing

Marianna Tortorelli

Signature

Date: May 17, 2004

BEST AVAILABLE COPY

RECEIVED
CENTRAL FAX CENTER

MAY 17 2004

800.0052
A1141

OFFICIAL

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Marchand et al.
Serial No.: 09/853,989
Filed: May 11, 2001
For: METHODS AND APPARATUS FOR POWER CONTROL IN A SCALABLE
ARRAY OF PROCESSOR ELEMENTS
Group: 2116
Examiner: Perveen, Rehana

Durham, North Carolina
May 17, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Response to Restriction Requirement

Sir:

The present response replies to the Restriction Requirement mailed May 4, 2004. That requirement required restriction to one of six inventions categorized in groups as follows:

- Group I: Claims 1-4, drawn to a method for power control in a scalable pipelined array processor, classified in class 713, subclass 300.
- Group II: Claims 5-13, drawn to a reconfigurable register file system, classified in class 713, subclass 100.
- Group III: Claims 14-21, drawn to a method for conserving power where during conditional execution of multi-cycle instructions execution conditions are determined during decode, classified in class 713, subclass 320.

BEST AVAILABLE COPY

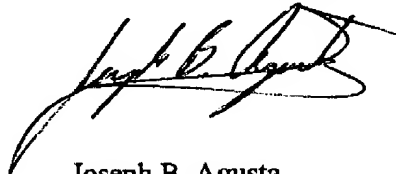
Appl. No. 09/853,989
Amdt. dated May 17, 2004
Reply to Office Action of May 4, 2004

- Group IV: Claims 22-24 and 26-29, drawn to a method for conserving power in conjunction with register file indexing, classified in class 713, subclass 320.
- Group V: Claims 25 and 30, drawn to a method for conserving power in conjunction with register file indexing of a partitioned VIM unit, classified in class 713, subclass 320.
- Group VI: Claims 31-34, drawn to a method and system for power control in a reconfigurable register file system, classified in class 713, subclass 300.

Applicants elect with traverse the Group I claims 1-4.

Please cancel claims 5-34 without prejudice to the presentation of these claims by way of divisional or otherwise.

Respectfully submitted,



Joseph B. Agusta
Reg. No. 52,547
5015 Southpark Drive, Suite 230
Durham, NC 27713
(919) 806-1600 x3